(40 pts) Approx. 2 days

Our fifth unit in digital electronics is all about creating complex outputs from digital circuits. In doing so, we can use segmented displays like the 7-segment display to show numbers and letters. Here we'll learn about the 7-segment display, multiplexers (MUX), demultipleers (DEMUX), as well as XOR, and XNOR logic gates! We've built a good foundation for the circuit design process: our job now is to add more tools to our circuit design toolkit. This unit will expose us to a variety of these tools and will have us build a lot of simulations to illustrate how these tools work.

- 1. Start by watching the presentations Displays Overview, 7-Segment Displays, and 7-Segment Display Driver. These will give a big overview of everything we should know and understand about 7-segment displays. Take at least a full page of notes on these topics, including a map for the 7-segment display and a diagram of how the 7-segment display driver works.
- 2. Complete the 7-Segment Display Assignment. Make sure that you show your work as you move through the assignment!
- 3. Create the 7-segment display circuits in multisim and confirm that they work as intended. Use the truth tables to record your outputs based off the many possible inputs.
- 4. Save your completed Multisim circuits to your jump drive.
- 5. Have Mr. Benshoof check-off your completed circuits before you move on!



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Part 1: Tasks	10 points	8-6 points	5-0 points
	+ Watch the <i>Displays Overview, 7-</i> Segment <i>Displays,</i> and <i>7-Segment</i>	- Less than a full page of notes on displays	- Very brief or no notes in your engineering
7-Segment Display	Display Driver presentations.	- Notes are missing	notebook
Notes	+ Take a full page of notes on	important parts	
	these topics including details		
	about the display and display		
	driver pins		
	15 points	16-10 points	9-0 points
	+ Complete the 7-Segment	- Assignment incomplete	- Assignment missing
	Displays Assignment	- Assignment not	- Assignment totally
7-Segment Displays	+ You showed your work	corrected	incomplete
Assignment	throughout the Assignment	- Truth tables not	- No work shown
	+ You completed the appropriate	complete	
	truth tables		
Multisim Circuits	+ You completed the Multisim	- Your simulations are	- You only simulated 1
	circuit for the basic 7-segment	not complete	circuit
	display	- Your simulations are	- You did not simulate
	+ You completed the Multisim	not correct	any circuits
	circuit for the display driver		

DE Unit 4: Displays Unit Due Date: November 8, 2019